

R09

Code No: 09A50402

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, HYDERABAD

B. Tech III Year I Semester Examinations, June/July-2014

COMPUTER ORGANIZATION

(Common to ECE, ETM)

Time: 3 hours

Max. Marks: 75

**Answer any five questions
All questions carry equal marks**

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- 1.a) Explain about error detection with odd parity bit.
- b) Write about bus structures.

- 2.a) Explain various logic micro operations.
- b) What is RISC?

- 3.a) Explain the selection of address for control memory along with flow chart.
- b) What is the difference between a micro processor and a micro program?

4. Explain the operation of BCD adder along with figure.

- 5.a) Explain set associative mapping of cache memory.
- b) Write about RAID.

- 6.a) Explain IOP mode of data transfer.
- b) Write about RS232.

- 7.a) Explain array processor.
- b) What is meant by memory interleaving?

- 8.a) Explain multiport by memory organization.
- b) What is cache coherence?
