...Code No: 54020

Loge No: 54040 JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B.Tech II Year II Semester Examinations, May - 2016

ELECTRONIC CIRCUIT ANALYSIS

(Common to ECE, EIE, ETM)

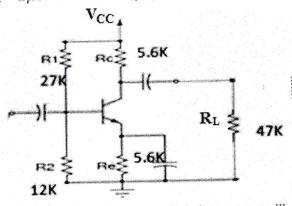
Time: 3 hours

Max. Marks: 75

Answer any five questions All questions carry equal marks

Draw the Common base configuration circuit diagram, input and output 1.a) characteristics and also find its h parameters.

The common Emitter circuit shown in figure has transistor parameter: $h_{ie} = 1 \text{K}\Omega$, 18+71 $h_{fe} = 85$, and $h_{oe} = 2\mu S$. Galculate A_v , Z_i and Z_o .



Draw the circuit diagram of direct coupled two stage circuit and explain its 2.a)

What are the advantages of Darlington amplifier? Draw its circuit diagram and b) explain its working.

Show how a capacitor may be used to obtain a desired upper cutoff frequency in 3.a) a transistor common emitter amplifier?

Explain the significance of gain bandwidth product in an amplifiers. [10+5]b)

Draw the small signal model of common drain amplifier and find its voltage gain. .4:a). What is folded cascade amplifier? Draw its circuit diagram and explain itsb) [7+8]working.

Explain how negative feedback will improve the stability of the amplifier? 5.a)

Derive an equation for the voltage gain of an amplifier that uses series voltage b) negative feedback. Draw any such type of circuit.

Draw the RC phase shift oscillator and derive the equation for oscillations. 6.a)

Design a Hartley oscillator to produce a 100 kHz output frequency with an b) amplitude of approximately ±8V. For simplicity, assume that there is no mutual inductance between L_1 and L_2 .

ĸ	b)	Sketch the basic convaveforms and also waveforms and also what is heat sink? What is cross over di	explain its operating it is used in p	tion. oower amplifiers'		aw::typical	SE
F	8.a). 	Draw the circuit diag What are the advanta	ram of Single tu ges of stagger tu	ned amplifier and ned amplifier?	d explain its opera	ation. [10+5]	C.P.
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